

AMENDMENTS TO THE CLAIMS:

This listing of claims replaces all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Currently Amended) ~~Front end~~ A circuit ~~[[(DX)]]~~ configured for use with at least two mobile wireless systems with different frequency bands, a frequency band being assigned to each mobile wireless system, the circuit comprising:

~~[[-]]~~ with a common an antenna connection (ANT_{in}) arranged on [[the]] an input side,

~~[[-]]~~ a first signal path electrically connected to the antenna connection and having an assigned first frequency band, the first signal path comprising:

a first output terminal that is configured to connect to at least one secondary stage circuit; and

a first band-pass filter between the antenna connection and the output terminal, the first band-pass filter comprising thin-layer resonators; and

a second signal path electrically connected to the antenna connection and with at least two signal paths (RX1, RX2) electrically connected to the antenna connection (ANT_{in}) and arranged to in parallel with the first signal path, the second signal path one another, one signal path being arranged in a mobile wireless system having an assigned second frequency band that is different from the first frequency band, the second signal path comprising: [[and]]

~~with individual electric gates (RX1_{out}, RX2_{out}) for each signal path,~~
~~arranged on the outer side, with these gates being connectable to~~ a second output
terminal configured connect to at least one secondary stage circuits circuit; and
~~where a unique frequency band is assigned to each signal path (RX1,~~
~~RX2),~~
~~where a~~ second band-pass filter (F1, F2) is arranged in each signal path
between the antenna connection and the second output terminal, the second band-
pass filter comprising (RX1, RX2), where the band-pass filter (F1, F2) essentially
contains thin-layer resonators. (RE) and is directly connected to the antenna
connection,

2. (Currently Amended) The Front-end circuit according to of claim 1, further
comprising in which a balun is connected in at least one of the first and second signal paths
(RX1, RX2).

3. (Currently Amended) The Front-end circuit according to of claim 2, wherein in
which the balun is connected in at least one of the first and second signal paths (RX1, RX2)
between a [[the]] band-pass filter (F1, F2) and an output terminal. the corresponding electric
gates (RX1_{out}, RX2_{out}).

4. (Currently Amended) The Front-end circuit according to one of claims 1 to 3, of claim 1, wherein at least one of the first and the second in which the band-pass filters filters (F1, F2) exhibits is configured to provide balun functionality.

5. (Currently Amended) The Front-end circuit according to one of claims of claim 1 to 4, wherein in which at least two of the thin-layer resonators in each of the first and second band-pass filters are stacked on top of one another and/or acoustically coupled with one another, thereby forming to form a compound resonator.

6. (Currently Amended) The Front-end circuit according to one of claims of claim 1 to 5, wherein each of the first and the second frequency band is separated from the respective other one with a selectivity of which guaranties a separation of frequency bands with a selection of at least about 20 dB.

7. (Currently Amended) The Front-end circuit according to one of claims 1 to 6 of claim 1, further comprising a duplexer in at least on of the first and second signal paths, in which a plurality of band-pass filters (F1, F2) having thin-layer resonators and connected to a duplexer (D1, D2) are arranged in at least one of the signal paths (RX1, RX2), and wherein the first signal path comprises a where this signal path (RX1, RX2) exhibits a first reception path $[(RX)]$ and a first transmission path $[(TX)]$ and the second signal path comprises a second reception path and a second transmission path.

8. (Currently Amended) The circuit of Diplexer module according to claim 7,
further comprising a low noise amplifier (LNA) in at least one of the first and second signal
paths, the LNA being downstream from the duplexer in a direction of signal propagation. where
an LNA [(V1)] is connected downstream from the duplexer (D1, D2) in the reception path.
(RX) and/or a power amplifier (V2) in the transmission path (TX).

9. (Currently Amended) The circuit of claim 8, further comprising
further comprising a power amplifier in at least one of the first and second signal paths,
the power amplifier being downstream from the duplexer in a direction of signal propagation.
where

Diplexer module according to claim 7 or 8, where an additional band pass filter (F11,
F21) is connected downstream from the duplexer (D1, D2), the LNA (V1) and/or the power
amplifier (V2).

10. (Currently Amended) The circuit of claim 1, wherein Diplexer module
according to one of claims 7 to 9, where, in at least one of the first and the second signal paths
(RX1, RX2), the reception path (RX) and/or the transmission path (TX) are provided for is
configured to conduct[[ing]] a symmetrical signal.

11. (New) The circuit of claim 1, wherein the thin-layer resonators are acoustically coupled to form a compound resonator.

12. (New) The circuit of claim 7, further comprising an LNA disposed subsequent to a power amplifier in the transmission path.

13. (New) Circuitry comprising:

- an antenna;
- a first duplexer connected to the antenna;
- a second duplexer connected to the antenna;
- a first signal path comprising a first transmission path and a first reception path, the first transmission path comprising:
 - a first input terminal; and
 - a first band-pass filter between the first duplexer and the first input terminal;
- the first reception path comprising:
 - a first output terminal; and
 - a second band-pass filter between the first duplexer and the first output terminal; and
- a second signal path comprising a second transmission path and a second reception path;

the second transmission path comprising:

- a second input terminal; and

a third band-pass filter between the second duplexer and the second input terminal;

the second reception path comprising:

a second output terminal; and

a fourth band-pass filter between the second duplexer and the second output terminal.

14. (New) The circuitry of claim 13, further comprising:

a first low noise amplifier (LNA) between the first duplexer and first band-pass filter; and

a second low noise amplifier (LNA) between the first duplexer and third band-pass filter.

15. (New) The circuitry of claim 14, further comprising:

a third low noise amplifier (LNA) between the first duplexer and second band-pass filter;

and

a fourth low noise amplifier (LNA) between the first duplexer and fourth band-pass filter.